

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

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APPLICANT
David C. Chapman

FILING DATE
October 19, 1999

GROUP
2811

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>[Signature]</i>	5,550,748	08/27/96	Xiong, Xiao-Ming			03/22/94
	5,673,201	09/30/97	Malm, Richard LaVerne, et al			03/23/96
	5,258,920	11/2/93	Haller, Theodore R., et al.			12/26/89
	5,801,960	9/1/98	Takano, Midori, et al.			11/3/97

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<i>[Signature]</i>	WO 93/15471	8/5/93	US				X

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>[Signature]</i>	Adler, et al., "An Interactive Router for Analog IC Design", Proceedings. Design, Automation and Test in Europe (Cat. No. 98Ex123), Proceedings Design, Automation and Test in Europe, Paris, France, 23-26 Feb. 1998, pages 414-420, XP002132373, 1998, Los Alamitos, CA, USA, IEEE Comput. Soc., USA ISBN: 0-8186-8359-7
<i>[Signature]</i>	Goro Suzuki, et al., "A Practical Online Design Rule Checking System", Proceedings of the ACM/IEEE Design Automation Conference (DAC), US, New York, IEEE, Vol. Conf. 27, 1990, pages 246-252, XP000245013 ISBN: 0-89791-363-9

EXAMINER

DATE CONSIDERED

8-*August*-2000

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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